

sync point A

sync point B

sync point C

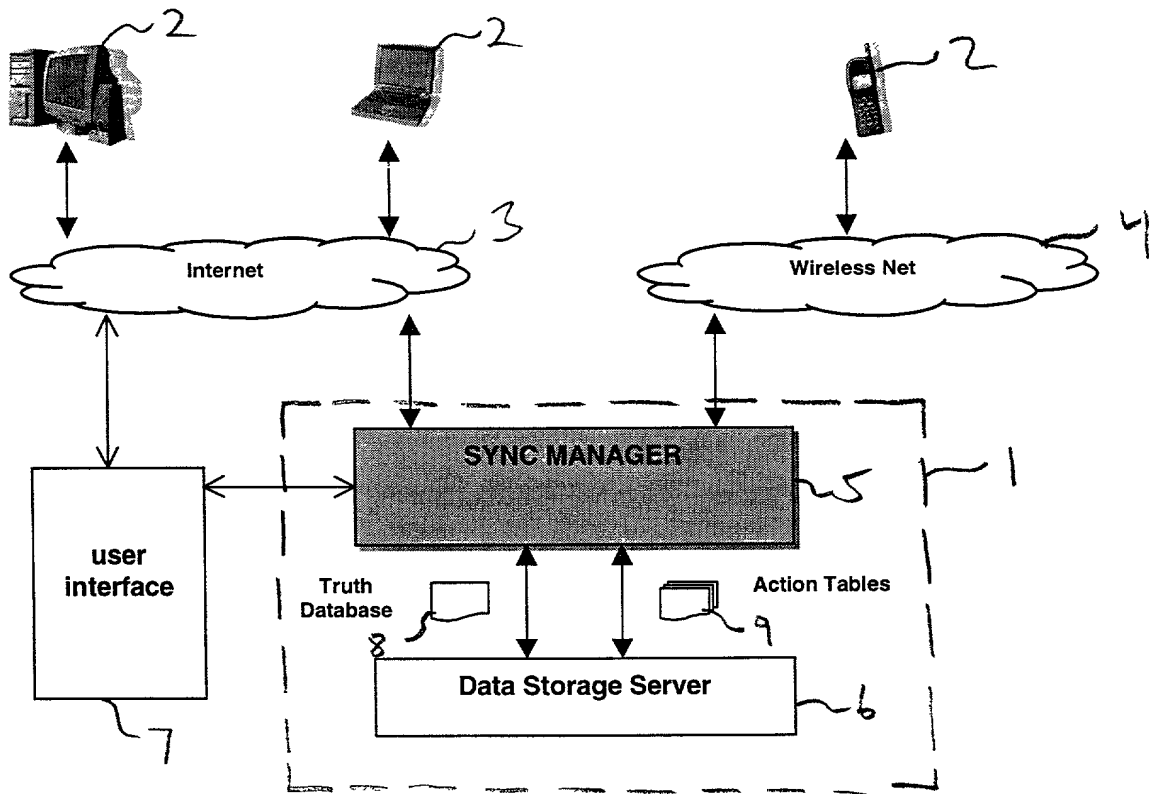


FIG. 1

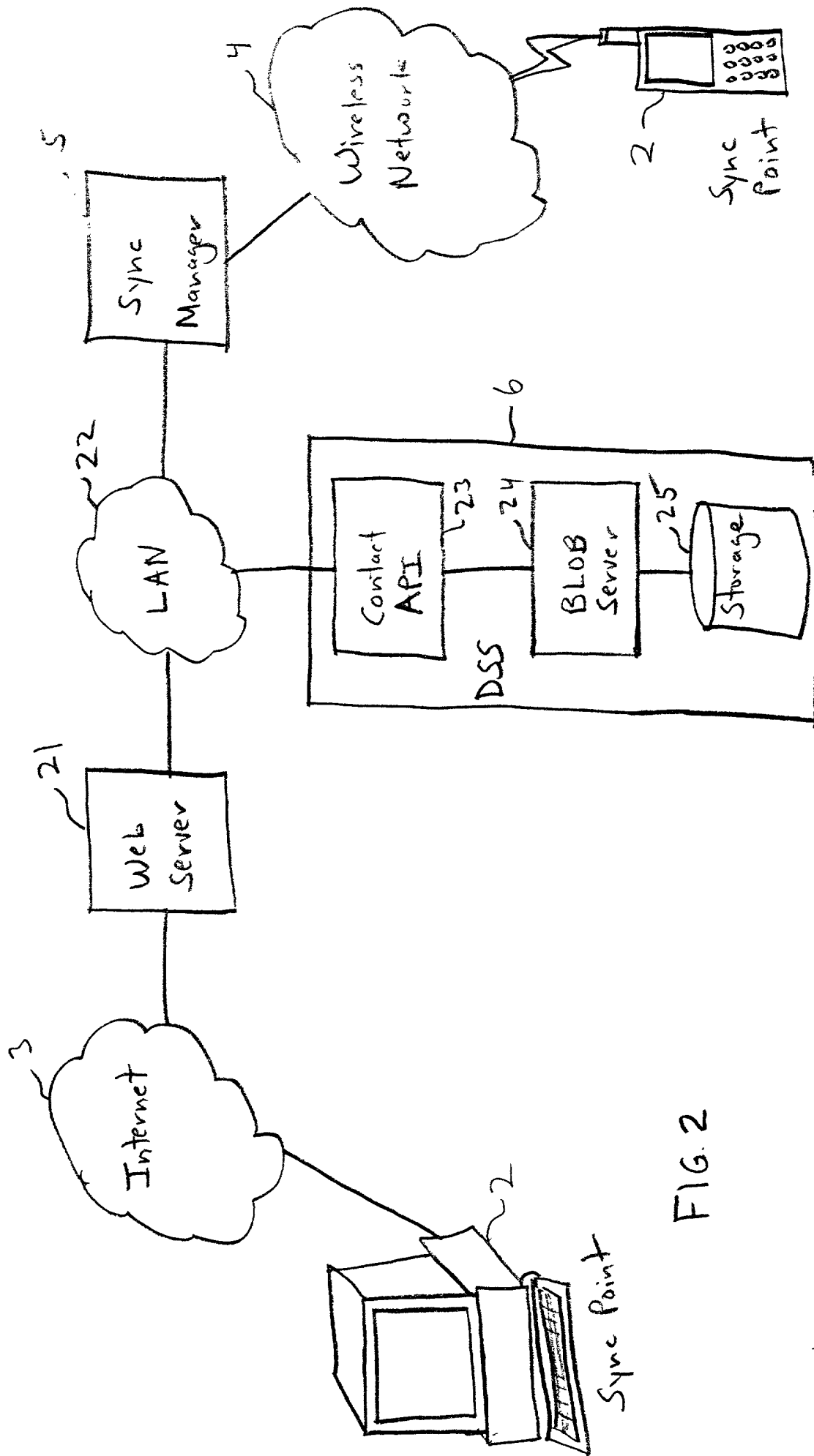


FIG. 2

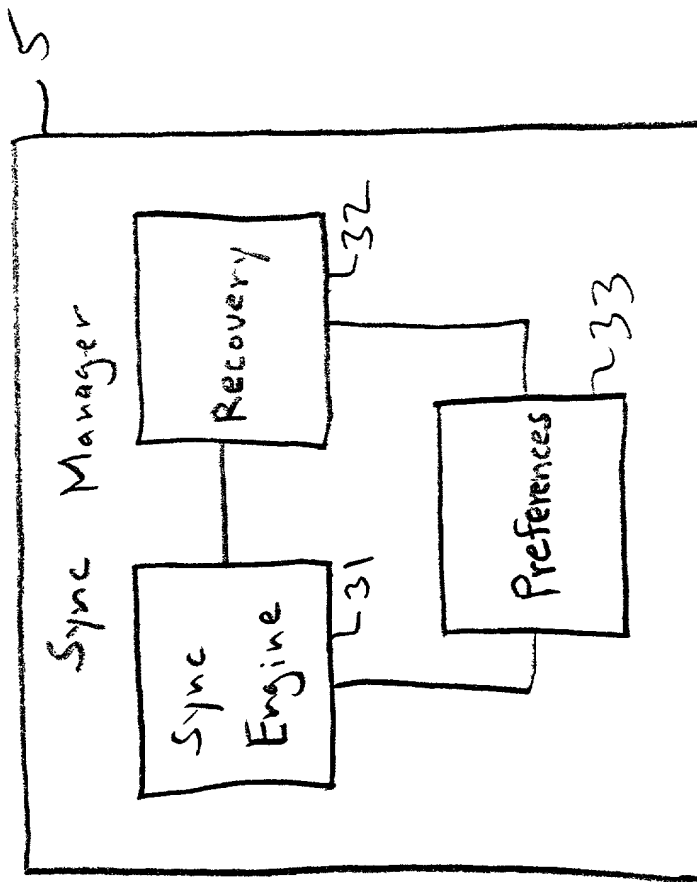


FIG. 3

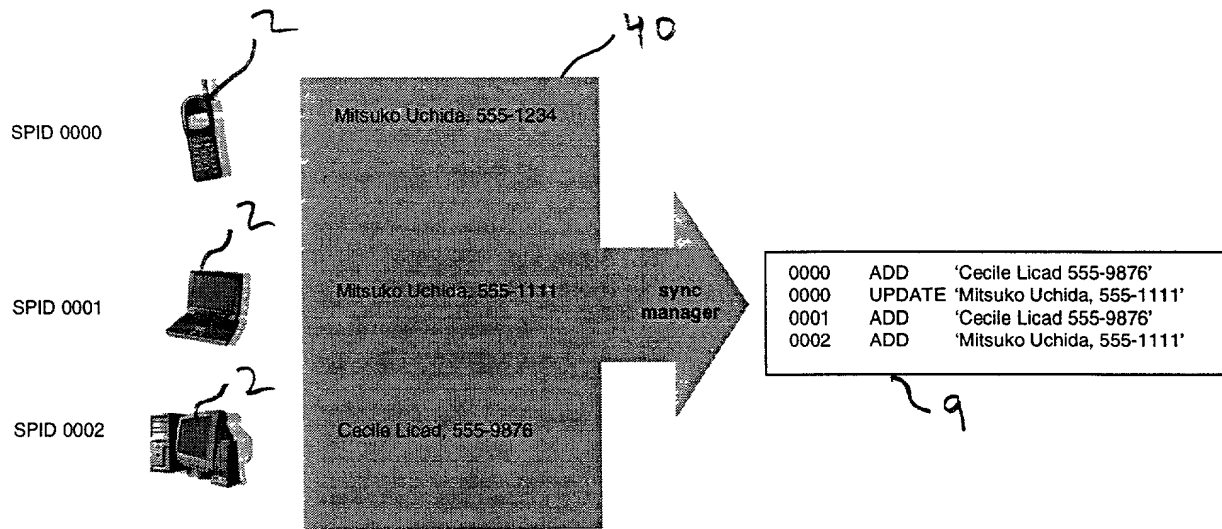


FIG. 4

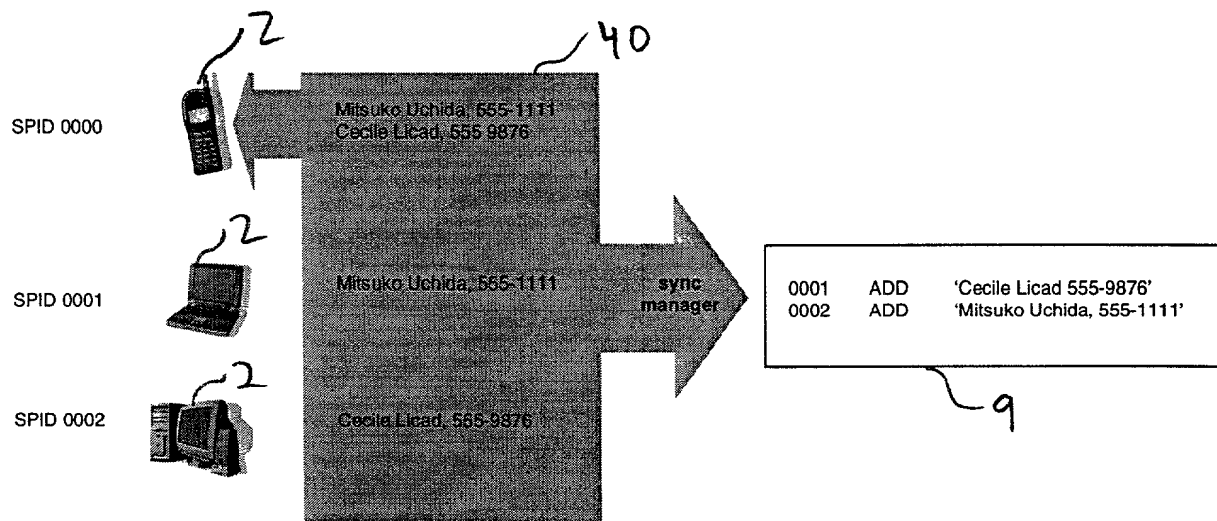


FIG. 5

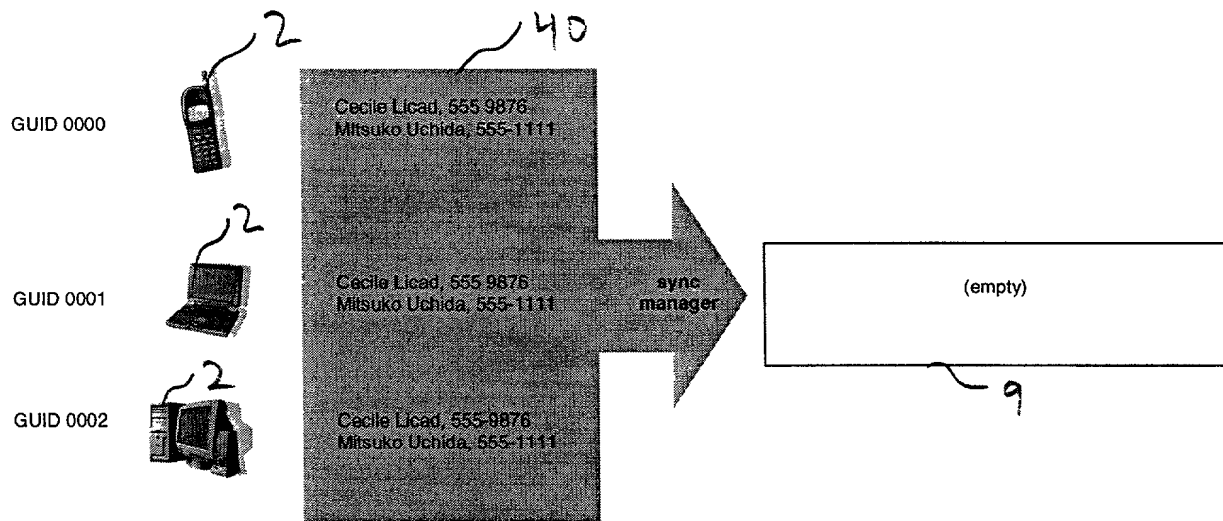


FIG. 6

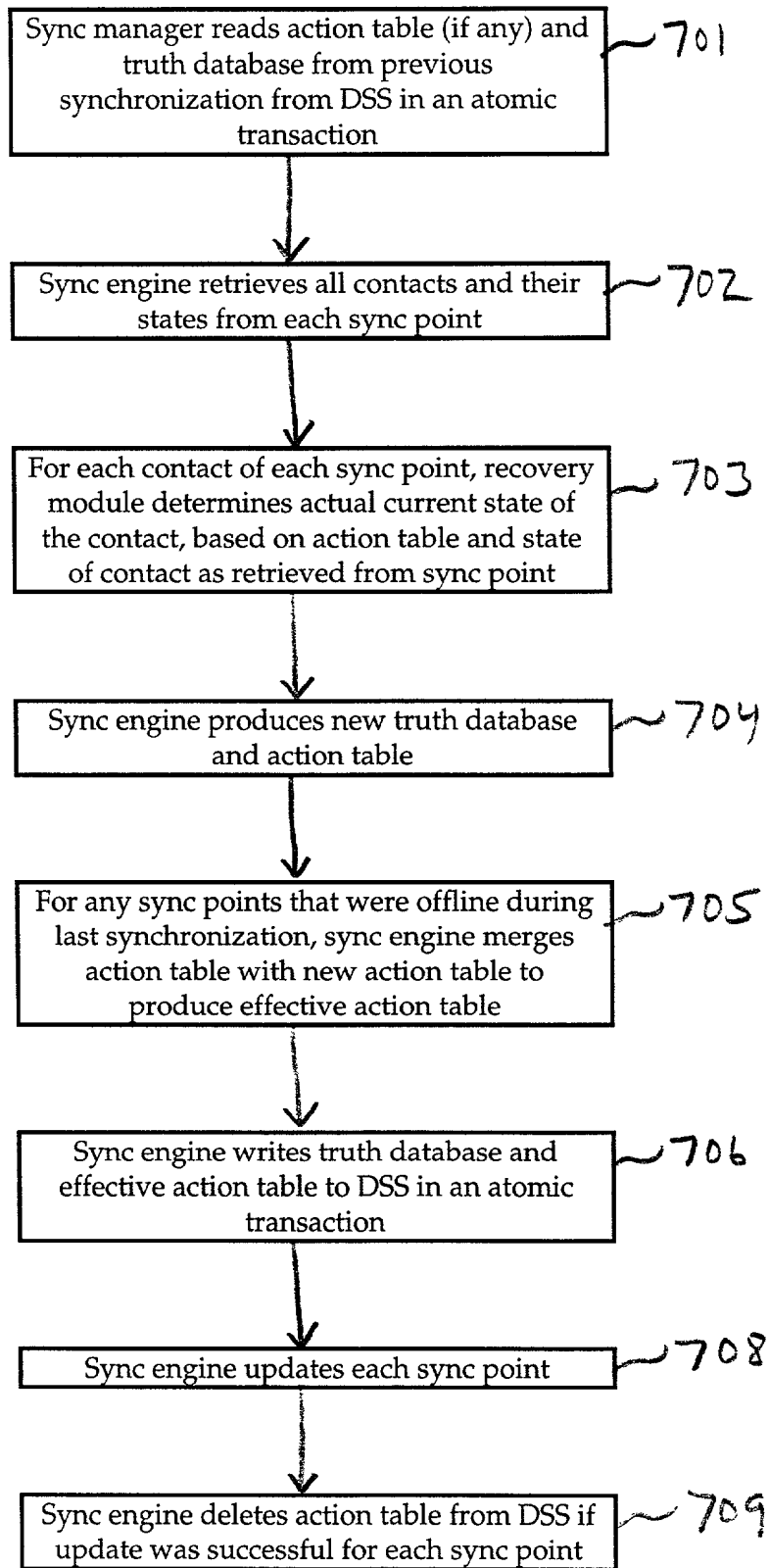


FIG. 7

703

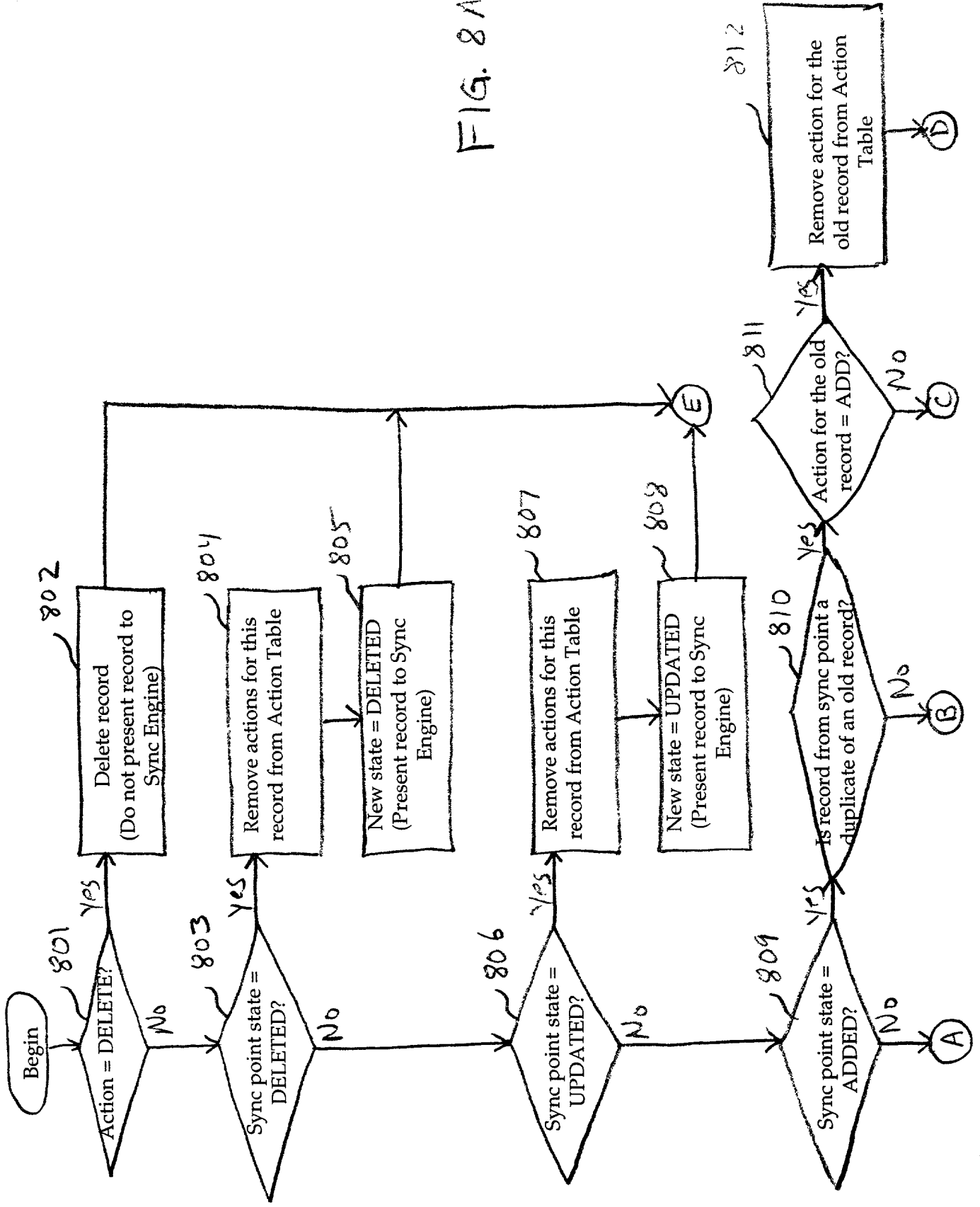


FIG. 8A

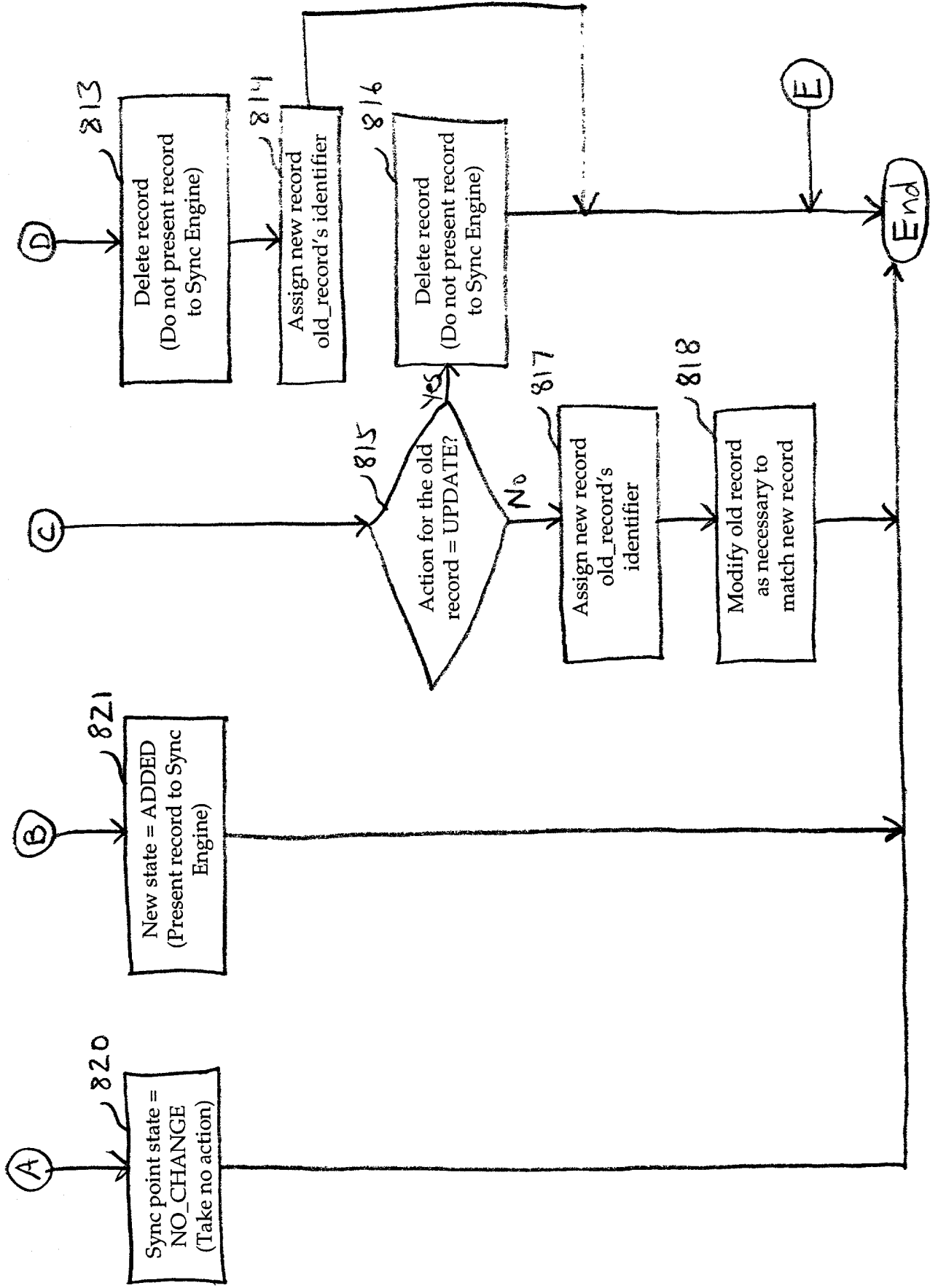


FIG. 8B



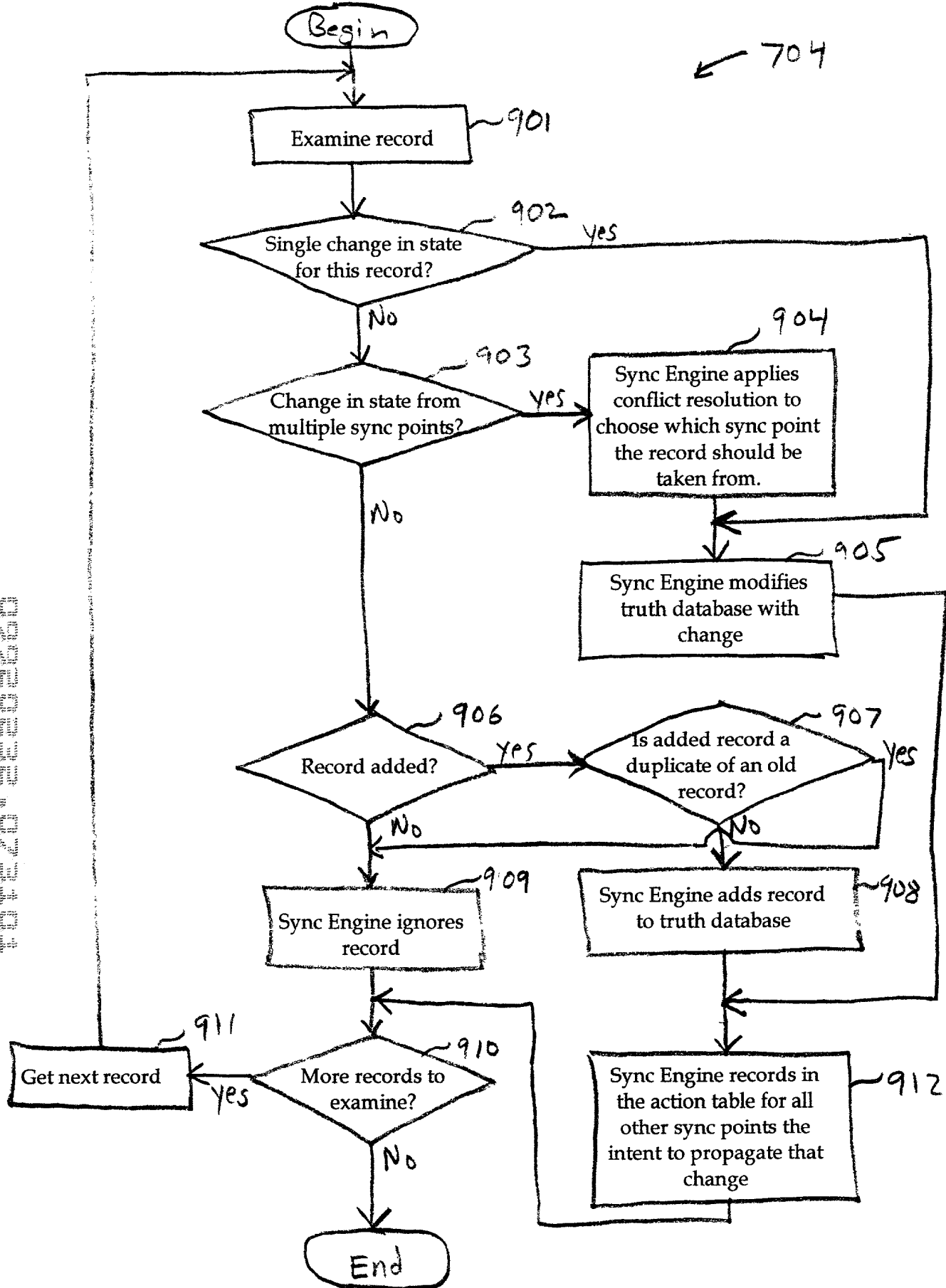


FIG. 9

FIG. 10 is a block diagram of a system architecture. The system includes a processor (101) connected to a system bus (107). The system bus (107) is connected to a ROM (102) and a RAM (103). The system bus (107) is also connected to a mass storage device (104) and an I/O device (105). The system bus (107) is further connected to a communication device (106). The mass storage device (104) and the I/O device (105) are both connected to the system bus (107). The communication device (106) is connected to the system bus (107) and to an external network (108). The ROM (102) and the RAM (103) are both connected to the system bus (107). The processor (101) is connected to the system bus (107). The system bus (107) is a central component that connects all the other components of the system.

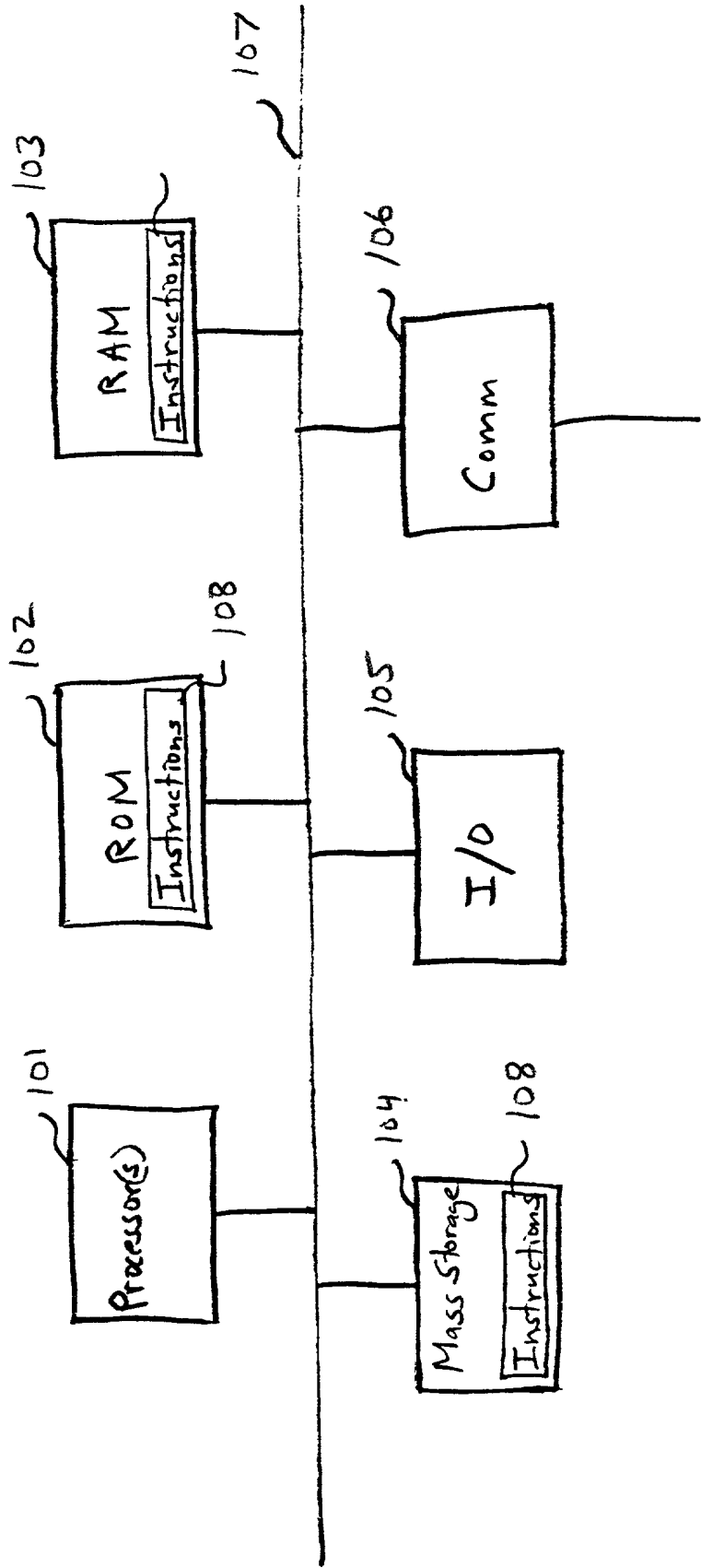


FIG. 10